

Geethanjali College of Engineering and Technology
(UGC Autonomous)
Department of ECE
II B.Tech I Sem Lab External Exams, November/December 2025
Academic Year 2025-26 (AR22 Regulation)

18th November 2025

Name of the Laboratory	Section/ Batch Number	Room No	Date of Exam	Time of Exam	Name of the Internal Examiner
Signals and Systems Lab (20EC21L01)	A1	317 Block I	20 th December 2025	9:00 am to 12:00 noon	Mr.R.Odaiah
	A2			11:00 am to 2:00 pm	
	A3			12:20 pm to 3:20 pm	
	B1	317 Block I	18 th December 2025	9:00 am to 12:00 noon	Mr.K.Victor
	B2			11:00 am to 2:00 pm	
	B3			12:20 pm to 3:20 pm	
Digital Design Lab (20EC21L02)	A1	113 Block I	18 th December 2025	9:00 am to 12:00 noon	Dr.Y.Nagalakshmi
	A2			11:00 am to 2:00 pm	
	A3			12:20 pm to 3:20 pm	
	B1	113 Block I	19 th December 2025	9:00 am to 12:00 noon	Ms.B.Sreelatha
	B2			11:00 am to 2:00 pm	
	B3			12:20 pm to 3:20 pm	
Electronic Circuit Analysis and Design Lab (20EC21L03)	A1	105 Block I	19 th December 2025	9:00 am to 12:00 noon	Mr.V.Amareswar
	A2			11:00 am to 2:00 pm	
	A3			12:20 pm to 3:20 pm	
	B1	105 Block I	20 th December 2025	9:00 am to 12:00 noon	Ms.J.Mrudula
	B2			11:00 am to 2:00 pm	
	B3			12:20 pm to 3:20 pm	


HoD, ECE


Dean, SE & CE